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DEC 13 2007

CUSTOMER NO.: 24498
Serial No. 10/576,389
Office Action dated: 09/28/07
Response dated: 12/13/07

PATENT
PU030243

Listing and Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

1. (Currently Amended) Apparatus for use in a wireless transmitter, the apparatus comprising:

an amplifier for amplifying a radio frequency (RF) signal to provide an RF output signal for transmission; and

a predistorter for injecting a distortion signal into the RF signal prior to amplification for use in linearizing the amplifier;

wherein the predistorter includes a phase shifter operating at less than twice a carrier frequency of the RF signal and further including a signal path for conveying a clock signal having a frequency less than twice a carrier frequency of the RF signal and wherein the predistorter is in the signal path of the clock signal;

wherein the phase shifter adjusts a phase of the clock signal and provides a phase-shifted clock signal, and wherein the predistorter further comprises:

a mixer responsive to the phase-shifted clock signal and an intermediate frequency (IF) signal for providing the distortion signal;

an amplitude adjuster coupled to the mixer for adjusting an amplitude of the distortion signal before application to the amplifier;

a directional coupler disposed between the amplitude adjuster and the amplifier for injecting the distortion signal into the RF signal; and

a processor for controlling the phase shifter and amplitude adjuster such that the distortion signal linearizes the amplifier.

Cancel claim 2.

Cancel claim 3.

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4. (Currently Amended) The apparatus of claim 31, wherein the processor is a digital signal processor.

5. (Currently Amended) The apparatus of claim 31, further including an upconverter for converting the IF signal to the RF signal.

6. (Currently Amended) The apparatus of claim 31, further including an amplifier for amplifying the distortion signal before application to the amplitude adjuster.

7. (Currently Amended) The apparatus of claim 31, wherein the predistorter includes no more than one directional coupler.

8. (Currently Amended) Apparatus for use in a wireless transmitter, comprising:
an amplifier for amplifying an RF signal and a distortion signal to provide an RF output signal for transmission, where the RF signal has a carrier frequency; and
a phase shifter for receiving a clock signal having a frequency less than twice the carrier frequency and for providing a phase-shifted signal, wherein a change in phase of the phase-shifted signal results in a change in phase of the distortion signal;
a radio frequency (RF) integrated circuit (IC) for providing the RF signal and the clock signal; and wherein the RF IC is responsive to the phase shifted signal for providing the distortion signal;
an amplitude adjuster for adjusting an amplitude of the distortion signal;
a directional coupler disposed between the amplitude adjuster and the amplifier for injecting the distortion signal into the RF signal; and
a processor for controlling the phase shifter and amplitude adjuster such that the distortion signal linearizes the amplifier.

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Cancel claim 9.

Cancel claim 10.

11. (Currently amended) The apparatus of claim ~~108~~, wherein the processor is a digital signal processor.

12. (Currently amended) The apparatus of claim ~~108~~, further including no more than one directional coupler in a circuit path between the amplifier and the RF integrated circuit.

13. (Currently amended) The apparatus of claim ~~108~~, further including an amplifier for amplifying the distortion signal before application to the amplitude adjuster.

14. (Previously presented) Apparatus for use in linearizing an amplifier of a wireless transmission system, wherein the amplifier amplifies a radio frequency (RF) signal for transmission, the apparatus comprising:

a source of a clock signal having a frequency less than twice a frequency of the RF signal;

a phase shifter responsive to the clock signal for providing a phase-shifted clock signal;

a distortion generator responsive to the phase-shifted clock signal for providing a distortion signal;

an amplitude adjuster responsive to the distortion signal for adjusting an amplitude thereof;

a coupler disposed between the amplitude adjuster and the amplifier for injecting the distortion signal into the amplifier; and

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a controller for controlling the phase shifter and the amplitude adjuster such that the distortion signal coupled into the amplifier linearizes the amplifier.

15. (Original) The apparatus of claim 14, wherein the phase shifter is coupled to the source via a signal path, which conveys the clock signal.

16. (Original) The apparatus of claim 14, further including an amplifier for amplifying the distortion signal before application to the amplitude adjuster.

Cancel claim 17.